

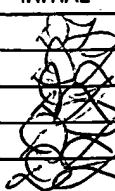
Form PTO 1449
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LIST OF REFERENCES CITED BY APPLICANT

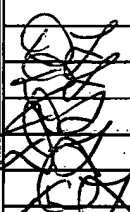
APPLICANT
Shinichi MURAKAWA, et al.FILING DATE
Herewith

GROUP

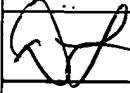
U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
	AA	6,459,284 B1	10/2002	Kumashiro			
	AB	5,493,238	2/1996	Takeuchi			
	AC	5,321,354	6/1994	Ooshima et al.			
	AD	6,323,653 B1	11/2001	Field et al.			
	AE	5,073,754	12/1991	Henley			
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						
	AL						
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	AN						

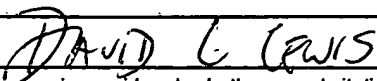
FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	TRANSLATION	
					YES	NO
	AO	5-11000	01/19/93	Japan (with English Abstract)		x
	AP	10-62474	03/06/98	Japan	x	
	AQ	64-35597	02/06/89	Japan	x	
	AR	10-104563	04/24/98	Japan	x	
	AS	11-153637	6/1999	Japan		
	AT					
	AU					
	AV					

OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, etc.)

	AW	Nobuo FUJII, vol. 13, pages 41-42, "ANALOG ELECTRONIC CIRCUIT INI INTEGRATED CIRCUIT ERA", May 10, 1990				
	AX					
	AY					
	AZ					<input type="checkbox"/> Additional References sheet(s) attached

Examiner



Date Considered

12/11/05

*Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.